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Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 34326	SERIAL NO. 10/016,455
INFORMATION DISCLOSURE CITATION BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT: Hedeki Uchida et al.	
		FILING DATE: December 10, 2001	GROUP ART UNIT: 2811

Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date If Appropriate
U6	A	5,297,333	03/1994	Kusaka			
V	B	5,783,870	07/1998	Mostafazadeh et al.			
	C						
	D						
	E						
	F						
	G						
	H						

		Document No.	Date	Country	Class	Subclass	Translation
U6	I	WO 00/05936	02/2000	PCT			English Text
	J						
	K						
	L						
	M						

	N	
	O	
	P	

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9/16/04

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U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date If Appropriate
UG	A	5,601,229	02/1997	Nakazato et al.			
UG	B	5,791,243	08/1998	Marcoux et al.			
	C						
	D						
	E						
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	G						
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		Document No.	Date	Country	Class	Subclass	Translation
UG	I	62 056145	03/1987	JP			English Abstract
UG	J	06 238869	08/1994	JP			English Abstract
UG	K	08 187836 A	07/1996	JP			English Abstract
UG	L	11 198348	07/1999	JP			English Abstract
	M						

OTHER REFERENCES (Including Author, Title, Date, Patent, Pages, Etc.)

UG	N	IBM Technical Disclosure Bulletin, IBM Corp. New York, "KNOWN GOOD DYE TEST AND DIRECT CHIP ATTACH ASSEMBLY", July 1, 1996, pages 215, 217, Vol. 39 No. 07
	O	
	P	

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